

# United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/006,044	12/04/2001	Gilbert C. Sih	010264	9617	
23696 7:	590 10/04/2006		EXAMINER		
QUALCOMM INCORPORATED			NGUYEN, HAU H		
5775 MOREHO SAN DIEGO.	OUSE DR. CA 92121		ART UNIT	PAPER NUMBER	
orav Dibboo,			2628		
			DATE MAILED: 10/04/200	6	

Please find below and/or attached an Office communication concerning this application or proceeding.

		Appl	ication No.	Applicant(s)				
		10/0	06,044	SIH ET AL.				
	Office Action Summary	Exan	niner	Art Unit				
			H. Nguyen	2628				
Period fo	The MAILING DATE of this commu or Reply	nication appears o	n the cover sheet	with the correspondence a	ddress			
THE - Exte after - If the - If NC - Failu Any	ORTENED STATUTORY PERIOD MAILING DATE OF THIS COMMUN nsions of time may be available under the provision SIX (6) MONTHS from the mailing date of this come period for reply specified above is less than thirty of period for reply is specified above, the maximum is tree to reply within the set or extended period for rep reply received by the Office later than three months ed patent term adjustment. See 37 CFR 1.704(b).	NICATION. us of 37 CFR 1.136(a). In umunication. (30) days, a reply within the statutory period will apply by will, by statute, cause the	no event, however, may a ne statutory minimum of the and will expire SIX (6) MC ne application to become	a reply be timely filed  airty (30) days will be considered time  DNTHS from the mailing date of this  ABANDONED (35 U.S.C. § 133).				
Status								
1)[🗆	Responsive to communication(s) file	ed on <i>08 Mav 200</i>	06.					
· · · · · · · · · · · · · · · · · · ·	This action is <b>FINAL</b> .	2b)⊠ This action						
3)	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.							
Disposit	ion of Claims							
5)□								
Applicati	ion Papers							
·	The specification is objected to by the							
10)	10) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner.							
	Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).							
11)	The oath or declaration is objected to	_	•	<del>-</del> ' ' ' ' ' ' ' ' ' ' ' ' ' ' ' ' ' ' '	, ,			
Priority ι	ınder 35 U.S.C. § 119							
a)(	Acknowledgment is made of a claim  All b) Some * c) None of:  1. Certified copies of the priority  2. Certified copies of the priority  3. Copies of the certified copies application from the Internationsee the attached detailed Office actions	or documents have or documents have of the priority document Document PCT	been received. been received in cuments have bee	Application No n received in this Nationa	l Stage			
Attachmen	, ,		4) 🗖 Jakan 3	Cumman, (DTO, 440)				
	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (	PTO-948)		Summary (PTO-413) (s)/Mail Date				
3) 🔲 Inform	nation Disclosure Statement(s) (PTO-1449 o r No(s)/Mail Date		5)  Notice of Other: _	Informal Patent Application (PT	O-152)			

Art Unit: 2628

### **DETAILED ACTION**

1. The Applicant's arguments in the Appeal Brief filed on May 8, 2006 has been fully considered and persuasive. The final rejection has been withdrawn. However, upon further consideration, a new ground of rejection is made as below.

# Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.
- 3. Claims 1-5, 7, 9-15, 17-20, 23-26, 28-35, 37 and 38 rejected under 35 U.S.C. 102(e) as being anticipated by Sita et al. (U.S. Patent No. 6,301,299).

As per claim 1, Sita et al. teach a memory having linearly addressable storage units to store video data (Figs. 2A, 2C, numeral 212, col. 5, ll. 51-62); and a programmable video direct memory access controller (Fig. 2C, numeral 214) to access the storage units of the memory in response to a command specifying a multidimensional block of video data (col. 5, line 63 to col. 6, line 13) and fetch the multidimensional block of video data from multiple non-contiguous rows of the memory in response to the command (Figs. 6A-6C, col. 15, lines 23 through col. 16, line 20, and col. 17, ll. 17-22, and also disclosed with reference to Fig. 10).

As per claim 2, Sita et al. teach the command specifying a number of rows and a number of columns for the block of video data (col. 23, ll. 18-50).

Art Unit: 2628

As per claims 3, Sita et al. teach the command specifying a jump parameter indicating the number of storage units between each row of the video block (col. 23, ll. 18-50).

As per claim 4, Sita et al. teach copying video data from the memory (212) to a destination memory (234, Fig. 2B) (col. 23, line 18 to col. 24, line 3, and col. 13, lines 47-48).

As per claim 5, Sita et al. further teach specifying a starting address of video data in the video block in the memory and in destination memory (col. 26, 1l. 42-50).

As per claim 7, Sita et al. teach a processor 216 issue commands to the VDMA controller 214 via a first bus; and a digital signal processor 218 issue commands to the VDMA controller via a second bus (Fig. 2A).

As per claim 9, Sita et al. disclose receiving a direct memory access command as cited above, from a processor 218 to transfer a multidimensional block of video data (macroblock), and further teach generating a set of source addresses and a set of destination addresses for the multidimensional block of video data in response to the command, wherein the set of source address correspond to multiple non-contiguous rows of a source memory (212) (as also cited above), and copying video data from the source memory (212) to a destination memory (234, Fig. 2B) according to the source addresses and destination addresses in respond to the command (col. 23, line 18 to col. 24, line 3, and col. 13, lines 47-48).

As per claim 10, as cited above, Sita et al. teach the source and destination memories each have linearly addressable storage units.

As per claim 11, as cited above, Sita et al. teach the command specifies a number of rows and a number of columns for the block of video data, and generating a set of addresses comprises

Art Unit: 2628

calculating the source addresses and destination addresses as a function of the number of rows and the number of columns (col. 23, ll. 18-50, with reference also to Fig. 22, col. 26, ll. 42-50).

As per claim 12, Sita et al. also teach the command specifies a jump parameter (an offset) indicating the number of addresses between each row of the video block, and generating a set of addresses comprising calculating the source address and destination address as a function of the jump parameter (col. 23, ll. 18-50).

As per claim 13, Sita et al. further teach specifying a starting address of video data in the source memory and in destination memory (col. 26, 1l. 42-50).

Claim 14, which is similar to claim 9, is thus rejected under the same rationale.

Claim 15, which is similar to claim 7, is thus rejected under the same rationale.

As per claims 17 and 18, which includes portions of claims 1 and 9, is discussed above, further requires a first memory to store a candidate video block to be encoded (such as reference block 122 as shown in Fig. 1E, col. 7, ll. 42-60), and a second memory to store a set of video data blocks (or a complete video frame) (Fig. 11), and a differential calculator to calculate differential metrics between the candidate video block and the set of video data block (e.g. calculating motion compensation, col. 24, ll. 17-34).

As per claim 19, Sita et al. teach an address generation logic (address generator 256) to read the candidate video data block from the first memory (as cited above, and col. 17, ll. 17-22), and one or more video blocks of the set of video data blocks from the second memory (Fig. 10 and 11).

Art Unit: 2628

As per claim 20, Sita et al. further teach the differential calculator reads the candidate video block from the first memory and one or more video blocks of the set of video blocks from the second memory in parallel (col. 3, ll. 13-23).

Claims 23-26, and 28, which are similar in scope to claims 2-5, and 7, are thus rejected under the same rationale.

As per claim 29, Sita et al. teach the differential calculator calculates the differential metrics (based on the motion vector) in response to search commands, and each search specifying a multidimensional region of video data (reference macroblock) stored with the second memory (col. 25, 1l. 36-65).

As per claim 30, Sita et al. teach a command buffer to store the search commands and deliver the search commands to the differential calculator (as cited above, and col. 14, ll. 44-46).

Claims 31-35, and 37-38, which are similar in scope to claims 9-16, are thus rejected under the same rationale.

## Claim Rejections - 35 USC § 103

- 4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
  - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 5. Claims 8 and 16 are rejected under 35 U.S.C. 103(a) as being unpatentable over Sita et al. (U.S. Patent No. 6,301,299)

As per claims 8 and 16, Sita et al. teach the motion estimation unit 314 (Fig. 3A) copying video data from memory 212 in response to the command. Sita et al. also teach the retrieved data

Application/Control Number: 10/006,044 Page 6

Art Unit: 2628

can be stored in a cache in memory interface 240 (col. 6, ll. 2-4). Thus, it would have been obvious one skilled in the art to utilize the method as taught by Sita et al. to include the cache in the motion estimation unit so that the cached video data can be easily retrieved for processing.

#### Conclusion

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Hau H. Nguyen whose telephone number is: 571-272-7787. The examiner can normally be reached on MON-FRI from 8:30-5:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kee Tung can be reached on (571) 272-7794.

The fax number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

H. Nguyen

9/29/2006

KEE M. TUNG SUPERVISORY PATENT EXAMINER